



**HASAN KALYONCU UNIVERSITY**  
**Faculty of Engineering**  
**Course Description Form**

<b>COURSE:</b> Digital Logic Design				
<b>CODE:</b> EE243		<b>SEMESTER :</b> FALL		
<b>LANGUAGE:</b> ENGLISH		<b>TYPE :</b> COMPULSORY		
<b>PRE-REQUISITES:</b> -	<b>THEORY</b>	<b>PRACTICAL</b>	<b>CREDIT</b>	<b>ECTS</b>
<b>CO-REQUISITES:</b> -				
<b>WEEKLY HOURS :</b>	3	2	4	6

**CONTENT OF THE COURSE:**

Dear students, welcome to digital logic design course. This course aims to provide the students with the necessary skills such as digital electronic circuit elements, drawing and analyzing logic diagrams, designing desired logic circuits, using hardware and software based modeling, using communication tools and designing, performing, analyzing and interpreting the experimental knowledge of the theoretical knowledge. The content, function, usage areas and working methods of the basic storage elements are explained. Evaluation of the course will be done with midterm, homework-laboratory and final exams.

**OBJECTIVE OF THE COURSE:**

To learn the necessary concepts for hardware of all systems from simple digital circuits to computer systems and gain the necessary hardware and design skills for digital analysis and digital circuit design.

**WEEKLY SCHEDULE**

<b>Week</b>	<b>Topics</b>
1	Digital Logic Concept, Concepts of Analog and Digital
2	Number Systems 1 (Decimal Number System, Binary Number System)
3	Number Systems 2 (Octal Number System, Hexadecimal Number System)
4	Alphanumeric Number Systems, Digital Codes, Logic Gates
5	Accuracy tables, symbols, electrical equivalent circuits of logic gates
6	Digital integrated circuit (IC) structures and used techniques
7	Boolean Algebra and simplification of logic functions
8	Midterm examination
9	Karnaugh Maps, its structure and types
10	Simplification of logic functions with Karnaugh Maps method
11	Simplification of logic functions with List method, Half and Full Adders
12	Encoders, Decoders, Converters, Multiplexers, Demultiplexers
13	State tables, State Diagrams, Latches and Flip-Flops and register types
14	General Evaluation

**TEXTBOOK:** M. Morris MANO (2015), Digital Design 5th Ed. ( ISBN: 978-0130621214), Pearson Pub. USA

**REFERENCE BOOKS:** - L. Thomas FLOYD (2011), Digital Fundamentals 10th Ed., Pearson Education, USA, Charles H. Rote (2016), - Fundamentals of Logic Design 7th. Ed. (ISBN: 978-0534954727), Cengage Learning., USA, - M..Morris Mano, C. KIME (2014), Logic and Computer

Design Fundamentals,4th Ed., Prentice Hall, 2008.

<b>EVALUATION SYSTEM:</b>		
<b>IN-TERM STUDIES</b>	<b>QUANTITY</b>	<b>PERCENTAGE (%)</b>
Midterm Exam	1	30
Homework	3	15
Laboratory works	13	15
Final Exam	1	40
<b>TOTAL</b>	<b>18</b>	<b>100</b>
CONTRIBUTION OF INTERM STUDIES TO OVERALL GRADE	17	60
CONTRIBUTION OF FINAL EXAMINATION TO OVERALL GRADE	1	40
<b>TOTAL</b>	<b>18</b>	<b>100</b>

<b>COURSE CATEGORY:</b>	<b>PERCENTAGE (%)</b>
Mathematics and Basic Sciences	40
Engineering	30
Engineering Design	30
Social Sciences	

<b>TABLE OF ECTS / WORKLOAD:</b>			
<b>Activities</b>	<b>QUANTITY</b>	<b>Duration (Hour)</b>	<b>Total Workload</b>
Course Duration	13	3	39
Hours for off-the-classroom study (Pre-study, practice)	14	6	84
Laboratory works	13	2	26
Mid-term	1	1,5	1,5
Final examination	1	1,5	1,5
Homework	3	4	12
<b>Total Work Load</b>			<b>164</b>
<b>Total Work Load / 30</b>			<b>5,46</b>
<b>ECTS Credit of the Course</b>			<b>6</b>

	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO10</b>	<b>PO11</b>
<b>LO1</b>	3	3	1	1	0	0	0	0	0	0	0
<b>LO2</b>	3	3	1	1	0	0	0	0	0	0	0
<b>LO3</b>	3	3	1	2	0	0	0	0	0	0	0
<b>LO4</b>	3	3	2	2	0	0	0	0	0	0	0
<b>LO5</b>	3	3	2	2	0	0	0	0	0	0	0
<b>LO6</b>	3	3	2	2	0	0	0	0	0	0	0
<b>LO7</b>	3	3	2	2	0	0	0	0	0	0	0
	PO: Program Outcomes   LO: Learning Outcomes Values: 0: None   1: Low   2: Medium   3: High										

<b>INSTRUCTOR(S):</b>	Asst. Prof. Dr. Ulaş GÜLEÇ
<b>FORM PREPARATION DATE:</b>	22.05.2019

<b>LEARNING OUTCOMES OF THE COURSE:</b>	<b>PROGRAM OUTCOMES:</b>
<p><b>LO1:</b> To understand the importance of digital systems in application and computer architecture</p> <p><b>LO2:</b> To learn the number systems used in digital system</p> <p><b>LO3:</b> To comprehend the basic theorems and axioms of Boolean algebra arithmetic</p> <p><b>LO4:</b> To learn the function and architecture of basic logic gate elements</p> <p><b>LO5:</b> To gain the ability of performing logic functions with basic logic gate circuits</p> <p><b>LO6:</b> To learn the simplification of numerical functions with different techniques (Boolean Algebra, Karnaugh Map Method, List Method,...)</p> <p><b>LO7:</b> To learn the internal structures and functions of basic storage elements</p>	<p><b>PO1:</b> Adequate knowledge in mathematics, science and engineering subjects pertaining to the relevant discipline; ability to use theoretical and applied knowledge in these areas in complex engineering problems.</p> <p><b>PO2:</b> Ability to identify, formulate, and solve complex engineering problems; ability to select and apply proper analysis and modeling methods for this purpose.</p> <p><b>PO3:</b> Ability to design a complex system, process, device or product under realistic constraints and conditions, in such a way as to meet the desired result; ability to apply modern design methods for this purpose.</p> <p><b>PO4:</b> Ability to devise, select, and use modern techniques and tools needed for analyzing and solving complex problems encountered in engineering practice; ability to employ information technologies effectively.</p> <p><b>PO5:</b> Ability to design and conduct experiments, gather data, analyze and interpret results for investigating complex engineering problems or discipline specific research questions.</p> <p><b>PO6:</b> Ability to work efficiently in intra-disciplinary and multi-disciplinary teams; ability to work individually.</p> <p><b>PO7:</b> Ability to communicate effectively in Turkish, both orally and in writing; knowledge of a minimum of one foreign language; ability to write effective reports and comprehend written reports, prepare design and production reports, make effective presentations, and give and receive clear and intelligible instructions.</p> <p><b>PO8:</b> Recognition of the need for lifelong learning; ability to access information, to follow developments in science and technology, and to continue to educate him/herself.</p> <p><b>PO9:</b> Consciousness to behave according to ethical principles and professional and ethical responsibility; knowledge on standards used in engineering practice.</p> <p><b>PO10:</b> Knowledge about business life practices such as project management, risk management, and change management; awareness in entrepreneurship, innovation; knowledge about sustainable development.</p> <p><b>PO11:</b> Knowledge about the global and social effects of engineering practices on health, environment, and safety, and contemporary issues of the century reflected into the field of engineering; awareness of the legal consequences of engineering solutions.</p>